

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(CRC redundancy adj check) with "with" with (data)	US-PGPUB; USPAT	OR	OFF	2005/06/26 17:02
L3	1739	(CRC redundancy adj check) with stor\$3 with (data)	US-PGPUB; USPAT	OR	OFF	2005/06/26 17:03
L4	0	(CRC redundancy adj check) with stor\$3 with (data) with integrity and "717"/\$.cccls.	US-PGPUB; USPAT	OR	OFF	2005/06/26 17:03
L5	78	(CRC redundancy adj check) with stor\$3 with (data) with integrity	US-PGPUB; USPAT	OR	OFF	2005/06/26 17:12
L10	1758	(memory ROM EPROM EEROM) near5 (secret confidential)	US-PGPUB; USPAT	OR	OFF	2005/06/26 18:40
L11	123	(memory ROM EPROM EEROM) near5 (secret confidential) adj data	US-PGPUB; USPAT	OR	OFF	2005/06/26 18:40
L12	10	(memory ROM EPROM EEROM) with plurality with (secret confidential) adj data	US-PGPUB; USPAT	OR	OFF	2005/06/26 18:41
L13	1	(ROM BIOS EPROM EEROM) with plurality with (proprietary secret confidential) adj data	US-PGPUB; USPAT	OR	OFF	2005/06/26 18:41
S1	807	(compar\$3 test\$3 check\$ verif\$3) with CRC with data with address	US-PGPUB; USPAT	OR	OFF	2005/06/23 19:55
S2	127	(compar\$3 test\$3 check\$ verif\$3) with CRC with data with address same circuit	US-PGPUB; USPAT	OR	OFF	2005/06/23 19:55
S3	18	(memory ROM EPROM EEROM) with (compar\$3 test\$3 check\$ verif\$3) with CRC with data with address same circuit	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:01
S4	24	(memory ROM EPROM EEROM) with (compar\$3 test\$3 check\$ verif\$3) with (checksum signature hash hmat) with data with address same circuit	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:02
S6	22	((memory ROM EPROM EEROM) with (compar\$3 test\$3 check\$ verif\$3) with (checksum signature hash hmat) with data with address same circuit) not S3	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:32
S7	1	"6085334".pn. and signature	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:26
S8	0	((memory ROM EPROM EEROM) with (compar\$3 test\$3 check\$ verif\$3) with (hmac) with data with address same circuit) not (S3 S6)	US-PGPUB; USPAT	OR	OFF	2005/06/26 18:39

S9	1288	((compar\$3 test\$3 check\$verif\$3) with (hmac MD didgest MASH hash signature CRC redundancy)) not (S3 S6)) and "711"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:39
S10	209	((compar\$3 test\$3 check\$verif\$3) with (hmac MD didgest MASH hash signature CRC redundancy) with address) not (S3 S6)) and "711"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:41
S11	143	((compar\$3 test\$3 check\$verif\$3) near3 (hmac MD didgest MASH hash signature CRC redundancy) with address) not (S3 S6)) and "711"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:42
S12	18	((compar\$3 test\$3 check\$verif\$3) near3 (hmac MD didgest MASH hash signature CRC redundancy) with address) not (S3 S6)) and "711"/\$.ccls. and "714"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:53
S13	2058	714/5.ccls. 714/6.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/24 10:45
S14	24	(bios with recovery).ab.	US-PGPUB; USPAT	OR	OFF	2005/06/24 10:52
S15	0	(bios with error with check).ab.	US-PGPUB; USPAT	OR	OFF	2005/06/24 10:48
S16	11	(bios).ab. and "711"/\$.ccls. and "714"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/24 10:55
S17	0	(bios near4 check\$3).ab. and "711"/\$.ccls. and "714"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/24 10:55
S18	1	(bios with check\$3).ab. and "711"/\$.ccls. and "714"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/24 10:56
S19	11	(bios with check\$3).ab. and "714"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/24 10:59
S20	35	bios with checksum and "714"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/24 11:00
S22	31	bios with checksum and "714"/\$.ccls. not S19	US-PGPUB; USPAT	OR	OFF	2005/06/24 11:02
S23	89	bios near4 post and "714"/\$.ccls. not S19	US-PGPUB; USPAT	OR	OFF	2005/06/24 11:08
S24	223	bios near4 architecture	US-PGPUB; USPAT	OR	OFF	2005/06/24 11:08
S25	21	bios near4 architecture and "714"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/24 11:09
S26	4	bios near4 checksum	EPO; JPO; DERWENT	OR	OFF	2005/06/24 11:28

S27	1	bios near4 CRC	EPO; JPO; DERWENT	OR	OFF	2005/06/24 11:18
S28	10	bios near4 CRC	US-PGPUB; USPAT	OR	OFF	2005/06/24 11:18
S29	0	bios near4 error adj correct\$4	EPO; JPO; DERWENT	OR	OFF	2005/06/24 11:29
S30	1	bios with error adj correct\$4	EPO; JPO; DERWENT	OR	OFF	2005/06/24 11:29
S31	2	bios near4 ECC	EPO; JPO; DERWENT	OR	OFF	2005/06/24 11:29
S32	8	bios near4 ECC	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 11:32
S33	2	bios near4 error adj (recover\$ correct\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 11:35
S34	1	DED-SEC	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 11:42
S36	1181	BIOS and error adj detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 14:32
S37	39	BIOS with error adj detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 14:48
S38	10	("6012127" "6026472" "6105053" "6289424" "6326973").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 14:57
S42	191	714/766.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 15:14
S43	133	714/766.ccls. and (MASH hmac MD fingerprint\$1 signature\$1 checksum CRC redundancy ECC)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 15:24
S44	47	714/766.ccls. and (MASH hmac MD fingerprint\$1 signature\$1 checksum CRC redundancy ECC)and memory.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 18:26

S50	8	714/766.ccls. and (MASH hmac MD fingerprint\$1 signature\$1 checksum CRC redundancy)and memory.ti. not ECC	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 18:19
S51	15	714/766.ccls. and ((MASH hmac MD fingerprint\$1 signature\$1 checksum CRC redundancy ECC) with address) and memory.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 18:26
S52	19	714/766.ccls. and ((MASH hmac MD fingerprint\$1 signature\$1 checksum CRC redundancy ECC) with address\$) and memory.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 18:36
S53	7	BIOS with address\$ with (verif\$ check\$) with error	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 18:44
S56	1	memory with includes with cell with blocks with store with n-bit with data with bit with address	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/24 19:31